

**AMENDMENTS TO THE CLAIMS**

**Listing of the claims:**

Following is a listing of all claims in the present application, which listing supersedes all previously presented claims:

1. (Currently Amended) A linear image sensor chip comprising:
  - a semiconductor substrate having an elongated shape;
  - an image pickup section formed on said semiconductor substrate, said image pickup section including (i) at least one photodiode group composed of a plurality of photodiodes formed only in a central area of a surface of said semiconductor substrate along a longitudinal direction of said semiconductor substrate and (ii) a charge transfer element provided for each said photodiode group;
  - a peripheral circuit section formed in a peripheral area of said surface of said semiconductor substrate, said peripheral area being external to said central area in the longitudinal direction of the semiconductor substrate;
  - a plurality of bonding pads formed on the surface of said semiconductor substrate externally, in the longitudinal direction, to the central area of the surface of the semiconductor substrate, each of said bonding pads having an exposed central surface area, wherein each of said bonding pads is disposed outer than said peripheral circuit section with respect to the longitudinal direction of said semiconductor substrate;
  - a plurality of metal lines formed on the surface of said semiconductor substrate, each of said metal lines having an end connected to one of said bonding pads and another end connected to said peripheral circuit or said charge transfer element;
  - a light-suppressing layer formed above said semiconductor substrate and covering a peripheral area of each of said plurality of photodiodes; and
  - a passivation layer formed to cover an outer surface area of each of said bonding pads.

2-3. (Canceled)

4. (Original) A linear image sensor chip according to claim 1, wherein said light-suppressing layer covers also said peripheral circuit section.

5. (Previously Presented) A linear image sensor chip according to claim 1, wherein said light-suppressing layer covers said metal lines at least in a region sideward along said at least one photodiode group.

6. (Original) A linear image sensor chip according to claim 1, wherein:  
said image pickup section includes four photodiode groups juxtaposed along a direction crossing the longitudinal direction;

said peripheral circuit section includes an output amplifier provided for each said charge transfer element and electrically connected to an output terminal of a corresponding charge transfer element; and

the linear image sensor chip further comprises a color filter array disposed for each of three photodiode groups of said four photodiode groups, said color filter arrays generally constituting a multicolor color filter array necessary for taking a color image.

7. (Original) A linear image sensor chip according to claim 6, further comprising a color filter array disposed above remaining one of said four photodiode groups.

8. (Currently Amended) A linear image sensor, comprising:  
a package including a bottom portion, sidewall portions and a lid portion generally defining an elongated inner space, and a plurality of lead electrodes, said lead electrodes extending from an end region of said elongated inner space, passing through said sidewall portions, and reaching an external space, said bottom portion and said sidewall portions being made of light shielding material and said lid portion having an elongated window made of transparent material;

a linear image sensor chip fixed in the inner space of said package, said linear image sensor chip including (1) a semiconductor substrate having an elongated shape

along a direction generally coincident with the longitudinal direction of said bottom portion, (2) an image pickup section formed on said semiconductor substrate, said image pickup section including (i) at least one photodiode group composed of a plurality of photodiodes formed only in a central area of a surface of said semiconductor substrate along a longitudinal direction of said semiconductor substrate and (ii) a charge transfer element provided for each said photodiode group, (3) a peripheral circuit section formed in a peripheral area of said surface of the semiconductor substrate, said peripheral area being external to said central area in the longitudinal direction of said semiconductor substrate, (4) a plurality of bonding pads formed on the surface of said semiconductor substrate externally, in the longitudinal direction, to the central area of the surface of the semiconductor substrate, each of said bonding pads having an exposed central surface area, wherein each of said bonding pads is disposed outer than said peripheral circuit section with respect to the longitudinal direction of said semiconductor substrate, (5) a plurality of metal lines formed on the surface of said semiconductor substrate, each of said metal lines having an end connected to one of said bonding pads and another end connected to said peripheral circuit or said charge transfer element, (6) a light-suppressing layer formed above said semiconductor substrate and covering a peripheral area of each of said plurality of photodiodes covering a peripheral area of each of said plurality of photodiodes;

    a passivation layer formed to cover an outer surface area of each of said bonding pads and of each of said bonding pads; and

    a plurality of bonding wires each electrically connecting one of said lead electrodes to a predetermined one of said bonding pads.

9. (Previously Presented) A linear image sensor according to claim 8, wherein all the bonding pads are formed on the surface of said semiconductor substrate outer than said at least one photodiode group with respect to the longitudinal direction of said semiconductor substrate.

10. (Canceled)

11. (Original) A linear image sensor according to claim 8, wherein said light-suppressing layer covers also said peripheral circuit section.

12. (Previously Presented) A linear image sensor according to claim 8, wherein said light-suppressing layer covers in plan said metal lines at least in a region sideward along said at least one photodiode group.

13. (Original) A linear image sensor according to claim 8, wherein:  
said image pickup section includes four photodiode groups juxtaposed along a direction crossing the longitudinal direction of said semiconductor substrate;  
said peripheral circuit section includes an output amplifier provided for each said charge transfer element and electrically connected to an output terminal of a corresponding charge transfer element; and

said linear image sensor chip further comprises a color filter array disposed for each of three photodiode groups of said four photodiode groups, said color filter arrays generally constituting a multicolor color filter array necessary for taking a color image.

14. (Original) A linear image sensor according to claim 13, further comprising a color filter array disposed above remaining one of said four photodiode groups.

15. (Original) A linear image sensor according to claim 8, wherein each said lead electrode is disposed outer than said image pickup section with respect to the longitudinal direction of said semiconductor substrate.

16. (Original) A linear image sensor according to claim 8, wherein each said lead electrode is disposed outer than said peripheral circuit section with respect to the longitudinal direction of said semiconductor substrate.

17. (Previously Presented) A linear image sensor chip according to claim 1, wherein said light-suppressing layer covers said metal lines at least in a region

sideward along said at least one photodiode group and an edge portion of each of said bonding pads.